

## ON LINE TESTING FOR VLSI%0A

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[On-line testing for VLSI: State of the art and trends](#)  
This paper discusses the state of the art and future trends of on-line testing techniques for VLSI. It cautions that emerging technological constraints and application requirements will expend

[On-Line Testing for VLSI \(eBook, 1998\) \[WorldCat.org\]](#)

On-Line Testing for VLSI contains a selected set of articles that discuss many of the modern aspects of on-line testing as faced today. The contributions are largely derived from recent IEEE International On-Line Testing Workshops. Guest editors Michael Nicolaidis, Yervant Zorian and Dhiraj Pradhan organized the articles into six chapters. In the first chapter the editors introduce a large [On-Line Testing for VLSI | SpringerLink](#)

On-Line Testing for VLSI is an edited volume of original research comprising invited contributions by leading researchers. Keywords ASIC CMOS Hardware Sensor VLSI communication integrated circuit single-electron transistor

[On-line Testing for VLSI A Compendium of Approaches ...](#)

This paper presents an overview of a comprehensive collection of on-line testing techniques for VLSI. Such techniques are for instance: self-checking design, allowing high quality concurrent checking

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#### **Partially based on Prof. Vishwani D. Agrawal lecture VLSI ...**

Partially based on Prof. Vishwani D. Agrawal lecture VLSI Testing and book by S. Mourad, Y. Zorian, "Principles of Testing Electronic Systems ECE 617.

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